

0057-2511-2YY

IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF :
SHUUCHI UENO ET AL / : EXAMINER: LOKE, S.
SERIAL NO: 09/395,184 / :
FILED: SEPTEMBER 14, 1999 / : GROUP ART UNIT: 2811 /
FOR: SEMICONDUCTOR DEVICE /
INCLUDING A WELL DIVIDED /
INTO A PLURALITY OF PARTS
BY A TRENCH

TECHNOLOGY CENTER 2800

MAY 20 2002

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AMENDMENT

ASSISTANT COMMISSIONER FOR PATENTS
WASHINGTON, D.C. 20231

SIR:

In response to the Office Action mailed February 19, 2002, please amend this
application as follows:

IN THE CLAIMS

Please cancel Claims 2 and 17 without prejudice.

The following is a clean copy of Claims 1, 14-16 and 20-23 with a marked-up copy
attached.

1. (Twice Amended) A semiconductor device comprising:

a semiconductor substrate;

an element isolation film formed such as to have a predetermined depth from a main
surface of said semiconductor substrate, said element isolation film dividing the area from
said main surface to said depth into a plurality of first regions;